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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/602,508	06/24/2003	John E. Smee	020515	2193	
QUALCOMM	7590 01/30/200 Incorporated	EXAMINER			
Attn: Patent De	partment	CORRIELUS, JEAN B			
5775 Morehous San Diego, CA		ART UNIT PAPER NU			
2 /			2611		
			MAIL DATE	DELIVERY MODE	
			01/30/2007	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

## Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)		
10/602,508	SMEE ET AL.		
Examiner	Art Unit		
Jean B. Corrielus	2611		

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		Jean B. Comeius		2011	
The MAILING DATE of this commun	ication appe	ars on the cover sheet v	with the c	orrespondence add	ress
THE REPLY FILED 18 January 2007 FAILS TO P	LACE THIS A	APPLICATION IN CONDIT	TION FOR	R ALLOWANCE.	
1.  The reply was filed after a final rejection, but this application, applicant must timely file on places the application in condition for allowa a Request for Continued Examination (RCE) time periods:	prior to or on e of the follow ence; (2) a No	the same day as filing a ving replies: (1) an amend tice of Appeal (with appea	Notice of dment, aff al fee) in o	Appeal. To avoid aba fidavit, or other evider compliance with 37 C	nce, which FR 41.31; or (3)
a) The period for reply expires 3 months from the	he mailing date	of the final rejection.			•
b) The period for reply expires on: (1) the mailin no event, however, will the statutory period for	g date of this A or reply expire la	dvisory Action, or (2) the dat ater than SIX MONTHS from	the mailing	g date of the final rejecti	on.
Examiner Note: If box 1 is checked, check eit TWO MONTHS OF THE FINAL REJECTION	I. See MPEP 7	06.07(f).			
Extensions of time may be obtained under 37 CFR 1.136 have been filed is the date for purposes of determining the under 37 CFR 1.17(a) is calculated from: (1) the expiration set forth in (b) above, if checked. Any reply received by may reduce any earned patent term adjustment. See 37 NOTICE OF APPEAL	he period of ex on date of the s the Office later	tension and the correspondin shortened statutory period for than three months after the	ng amount r reply orig	of the fee. The appropri inally set in the final Offi	iate extension fee ce action; or (2) as
2. The Notice of Appeal was filed on A filing the Notice of Appeal (37 CFR 41.37(a)) a Notice of Appeal has been filed, any reply	), or any exte	nsion thereof (37 CFR 41	.37(e)), to	avoid dismissal of th	
AMENDMENTS					
<ol> <li>The proposed amendment(s) filed after a fir         <ul> <li>(a) They raise new issues that would requ</li> <li>(b) They raise the issue of new matter (se</li> </ul> </li> </ol>	ire further co	nsideration and/or search	_	_	ecause
(c) They are not deemed to place the app appeal; and/or		•	aterially re	ducing or simplifying	the issues for
(d) They present additional claims without NOTE: (See 37 CFR 1.116 a	-	· -	finally rej	ected claims.	
4. The amendments are not in compliance with			of Non-Co	mpliant Amendment	(PTOL-324).
5. Applicant's reply has overcome the followin			J. 11011 OC	mphant / monamon	(
6. Newly proposed or amended claim(s) non-allowable claim(s).			separate,	timely filed amendme	ent canceling the
7.  For purposes of appeal, the proposed amen how the new or amended claims would be re The status of the claim(s) is (or will be) as fo	ejected is pro			II be entered and an e	explanation of
Claim(s) allowed: Claim(s) objected to:					
Claim(s) rejected: <u>1,2,4-7 and 9-26</u> .					
Claim(s) withdrawn from consideration: AFFIDAVIT OR OTHER EVIDENCE	<b>•</b>				
<ol> <li>The affidavit or other evidence filed after a file because applicant failed to provide a showin was not earlier presented. See 37 CFR 1.11</li> </ol>	ng of good an				
<ol> <li>The affidavit or other evidence filed after the entered because the affidavit or other evider showing a good and sufficient reasons why i</li> </ol>	date of filing	vercome all rejections un	nder appe	al and/or appellant fai	ils to provide a
10. ☐ The affidavit or other evidence is entered. A REQUEST FOR RECONSIDERATION/OTHER	An explanatio	n of the status of the clain	ns after e	ntry is below or attach	ned.
11.   The request for reconsideration has been of	considered bu	t does NOT place the app	plication i	n condition for allowa	nce because:
See Continuation Sheet.  12.  Note the attached Information Disclosure S	Statement(s).	(PTO/SB/08) Paper No(s)	)		
13.  Other:				4 -	
				Je B Corrielus  Brimany Examinar	blu

Primary Examine Art Unit: 2611

Continuation of 11. does NOT place the application in condition for allowance because: Applicant argues that the specification, as filed, paragraphs 34-38, provides support for the limitation "estimates for a subset of the plurality of chips", recited in claim 4. However a review of such section of the specification does not show such a support. More specifically, paragraph 34 as reproduced herewith, "[34] Knowledge of this structure allows the use of decision directed adaptation during the data portion of the frame in order to continuously calibrate the slicing of chips by the chip slicer 60. Thus, the symbol estimates created by the chip slicer 60 can be improved in fifty percent (50%) of the estimates", only teaches that the symbol estimates created by the chip slicer can be improved by fifty percent. It is further alleged that the specification, paragraphs 27-38 provides support for the limitations " "one or more", as recited in the claims, however a review of such paragraphs of the specification dos not such a support. In addition, the specification does not have support for the limitations as recited in claim 24. Therefore the rejection is maintained. It is the applicant's position that the specification, at paragraphs 27-35 and 38, provides support for the following limitations of "an identification of the chip based in part on a correlation to one or more chips in the code". However, a review of such section of the specification does not show the identification is based in part on a correlation to one or more chips. It is alleged that the specification, paragraph 25, as filed, provides support for the limitation "(a feedback filter configured to determine) noise component based in part on the one or more chip identification output from the slicer". However, it is noted that such section of the specification only teaches that "(the feedback filter configured to determine) the noise component based on sliced chips. In addition, the specification does not teach the limitations ""identify a chip value based in part on a correlation among at least a portion of chips" and "chip value from the slicer". Applicant further asserted that the portion of the disclosure relied upon by the examiner refers to a particular type of codeword, CCK codewords and that not all codewords have correlation among the chips. However, it is noted that page 3, line 2 does not refers to any particular codeword or CCK. Such teaching of the specification only refers to "codewords" in general. In response to applicant's request for support that "codewords have inherently correlation among the chips" see applicant's own admission at page 3, line 2.